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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10064306	FILING DATE 07/01/2002	CLASS 344	SUBCLASS 205	GAU 2186	EXAMINER AUDUONG
<p>**APPLICANTS: Brennan Ciaran; Fifield John; Stephens Jeremy; Storaska Daniel;</p> <p>365 7818</p>					
<p>**CONTINUING DATA VERIFIED:</p> <p>none</p>					
<p>** FOREIGN APPLICATIONS VERIFIED:</p> <p>none</p>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiner's Initials		[Signature]		FIS920010334	
TITLE : Writeback and refresh circuitry for direct sensed DRAM macro					

U.S. DEPT. OF COMM. PAT. & TM. PTO-435 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims <input type="checkbox"/> Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn Figs. Drawn Print Figs.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	Application Examiner
		PREPARED FOR ISSUE	
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